

## Power line chokes

SMD Rod Core chokes

1.7 ... 3.3  $\mu\text{H}$  / 10 kHz, 15 ... 28 A / +125 °C

**Series/Type:** B82116D\*A\*

**Ordering code:**

**Date:** January 2023

**Rated current: 15 ... 28 A / +125 °C**

**Rated Inductance: 1.7 ... 3.3  $\mu$ H / 10 kHz**



### Construction

- Rod core choke
- Ferrite core
- Single layer winding
- Core and winding glued
- Self-leaded SMD terminals

### Features

- High resonance frequency
- Enameled wire in accordance to EN 60317-13
- Wire class 200, UL listed
- Qualified according to AEC-Q200
- Suitable for reflow soldering
- RoHS compatible

### Applications

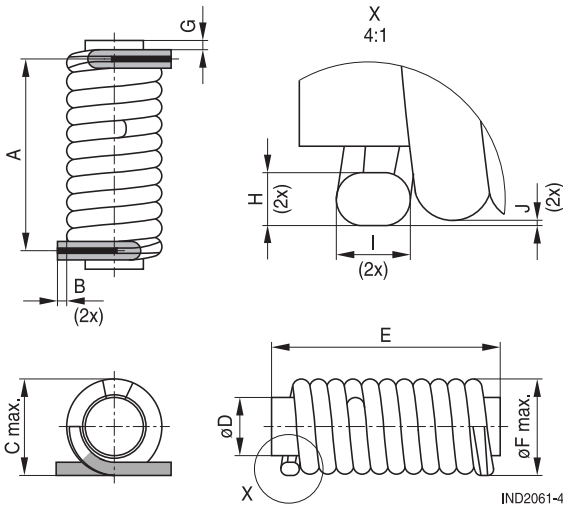
- EMC choke

### Terminals

- Ends of winding wire
- Pins hot dip tinned Sn99Cu

### Delivery mode

- Tape and reel

**Dimensional drawing**


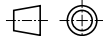
IND2061-4

	B82116D1831A101	B82116D1224A001	B82116D2820A003	B82116D2820A002
A	26.1±0.5	19±0.5	16±0.5	16.8±0.5
B	1.25±0.5	1±0.5	1±0.5	1±0.5
C	13.7	18.2	13	12
D	8±0.3	12±0.3	8±0.3	7.6±0.2
E	31±0.8	24±0.5	20±0.5	20±0.5
F	13.5	17.7	12.6	11.6
G	1.25±0.2	1.1±0.5	1±0.8	min. 0
H	1.8±0.1	2±0.1	1.4±0.1	1.25±0.1
I	2.5±0.2	2.8±0.1	1.8±0.1	1.8±0.2
J	0.1 +0.2/-0.1	0.1±0.1	0.1±0.1	0.1±0.1

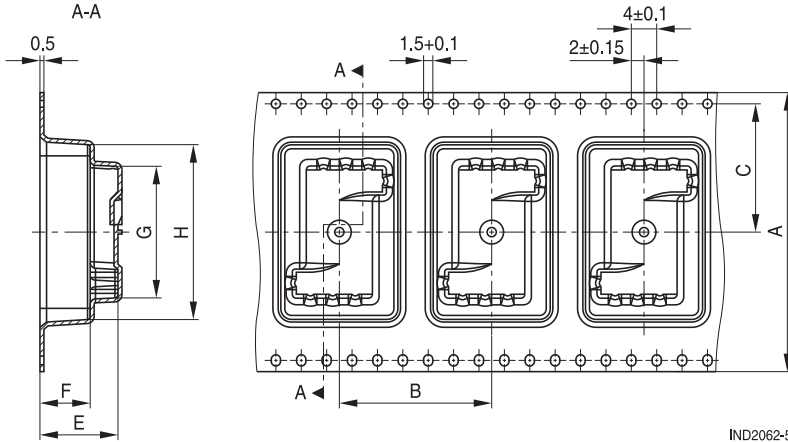
Part tolerances to ISO 2768-cL / ISO 8015.

Size ISO 14405 (E)

All dimensions in mm



IND1276-L-E

**Blister tape dimensional drawing**


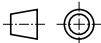
IND2062-5

	B82116D1831A101	B82116D1224A001	B82116D2820A003	B82116D2820A002
A	56	44	44	44
B	28±0.1	28±0.1	24±0.1	24±0.1
C	26.2±0.15	20.2±0.15	20.2±0.15	20.2±0.15
D	17.05+0.2/-0	20.55+0.2/-0	15.22+0.2/-0	14.85+0.2/-0
E	13.8+0.15/-0.05	18.4+0.15/-0.05	13.2+0.15/-0.05	12.3+0.15/-0.05
F	7.1+0.15/-0.05	7.9+0.15/-0.05	7.9+0.15/-0.05	7.4±0.1
G	31.85+0.2/-0	24.65+0.2/-0	20.66+0.2/-0	20.65+0.2/-0
H	38.9±0.2	30.45+0.2/-0	27.45+0.2/-0	27.45+0.2/-0
Parts/Reel	150	110	180	200

Reel	<p>62.4 max. 58.4 max. ø102 ø330</p> <p>IND2063-6</p>	<p>50.4 max. 46.4 max. ø102 ø330</p> <p>IND2064-7</p>
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Part tolerances to ISO 2768-cL / ISO 8015.

Size ISO 14405 (E)



All dimensions in mm

IND1276-L-E

**Technical data and measuring conditions**

Rated temperature $T_R$	+125 °C
Rated current $I_R$	15 ... 28 A (free-air convection cooling) <sup>1)</sup>
Nominal inductance $L_N$	Measured with Agilent 4284A, at 10 kHz, 1 V, at +20 °C
Inductance tolerance	±20% at +20 °C
Inductance decrease $\Delta L/L_0$	≤10% at DC magnetic bias with $I_R$ , +20 °C
DC resistance $R_{typ}$	Measured at +20 °C, typical value
Solderability (lead-free)	Sn96.5Ag3.0Cu0.5: (±245±3) °C, (3±0.3) s Wetting of soldering area ≥ 90% (to IEC 60068-2-58, test Td <sub>1</sub> , method 1)
Resistance to soldering heat	(+260 ±5) °C, (10±1) s (to IEC 60068-2-58, test Td <sub>2</sub> , method 1)
Operating temperature range	-40 °C ... +150 °C
Insulation voltage	Direct contact to metal parts of the customer application is not permissible. Insulation of winding surface cannot be warrant due to allowed leaks in winding wire accordance wire standard EN 60317-0-1.
Core material	Ferrit
Ferrite core surface irregularities	The standard IEC 60424-4 is the basis for the visual inspection of surface irregularities. These surface irregularities have no impact regarding function, manufacture ability and reliability of the component. No further spalling of core material permissible.

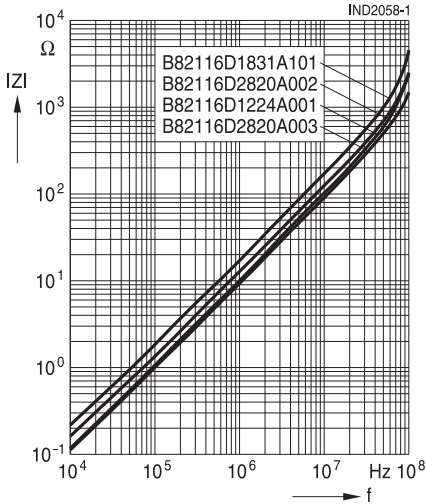
- 1) Current must be reduced when operating at higher ambient temperature than rated. See "Current derating" for details. Higher current can be applied by using an appropriate forced cooling approach. In any case, temperature of the coil is to be monitored and must not exceed the maximum value specified by the climatic category. The effect of magnetic saturation must be additionally considered when operated with higher current than specified.

**Characteristics and ordering codes**

$I_R$ A	$L_R$ μH	$R_{typ}$ mΩ	Inductance decrease $\Delta L/L_0$ A	Weight approx. g	Ordering code
19	3.3	1.8	40	11.0	B82116D1831A101
15	1.7	1.8	64	12.0	B82116D2820A003
28	1.8	1.2	110	28.5	B82116D1224A001
15	2.5	3.0	55	11.0	B82116D2820A002

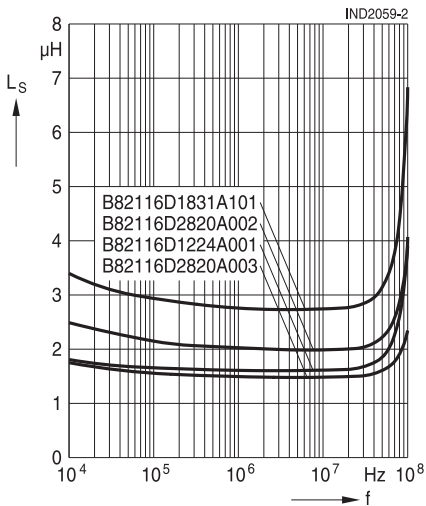
**Impedance |Z| versus frequency**

(Typical values measured at +20 °C)

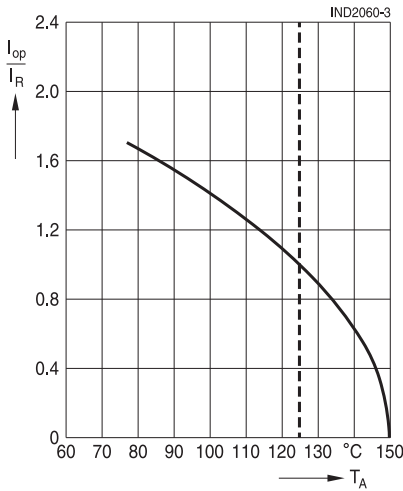


**Inductance  $L_s$  versus frequency**

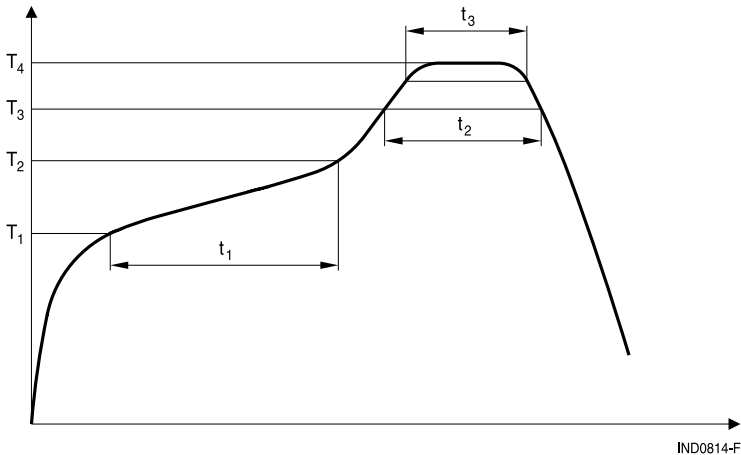
(Typical values measured at +20 °C)



**Current derating  $I_{op}/I_R$  versus ambient temperature  $T_A$** 

 (rated temperature  $T_R = +125\text{ °C}$ )

**Recommended reflow soldering profile**

Pb-free solder material (based on JEDEC J-STD 020D)



$T_1$ °C	$T_2$ °C	$T_3$ °C	$T_4$ °C	$t_1$ s	$t_3$ s	$t_3$ s
+150	+200	+217	+245	<120	<90	< 30 @ $T_4 - 5\text{ °C}$

### Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
  - Particular attention should be paid to the derating curves given there.
  - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.
- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation. Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.
- The following points must be observed if the components are potted in customer applications:
  - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
  - It is necessary to check whether the potting material used attacks or destroys the wire, wire insulation, plastics or glue.
  - The effect of the potting material can change the high-frequency behaviour of the components.
  - Many coating materials have a negative effect (chemically and mechanically) on the winding wires, insulation materials and connecting points. Customers are always obligated to determine whether and to what extent their coating materials influence the component. Customers are responsible and bear all risk for the use of the coating material. TDK Electronics does not assume any liability for failures of our components that are caused by the coating material.
- Ceramics / ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.
- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.
- Due to product design and applied manufacturing process, appearance, symmetry, and shape of not dimensioned details could vary within same lot, as well discoloration of housing is possible. TDK does not expect detrimental effects on product function or reliability. In case of conflicts, TDK reference standard shall prevail.

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2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
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## Important notes

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